

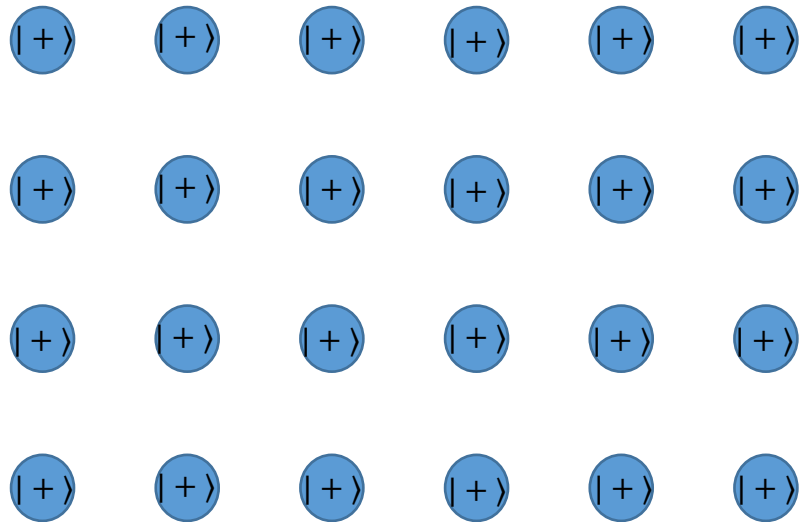
*Photonic approach for implementing  
Quantum Computer*

## *KLM protocol (CBQC)*

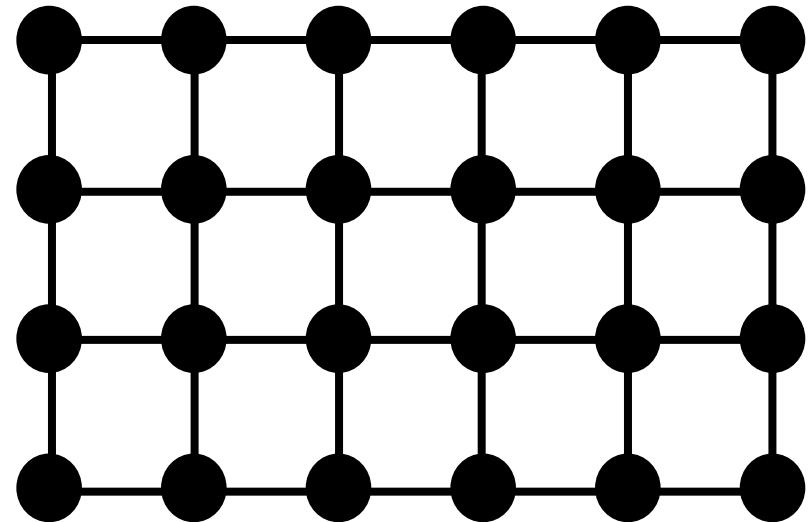
Due to restrictions of bosonic systems, it was believed that it is not possible to build a universal quantum computer using only linear optics, until in 2001, Knill, Laflamme and Milburn(KLM) realized that measurement on parts of the circuit can be used to evoke nonlinearity and still deliver scalability.

# Measurement based quantum computation (MBQC)

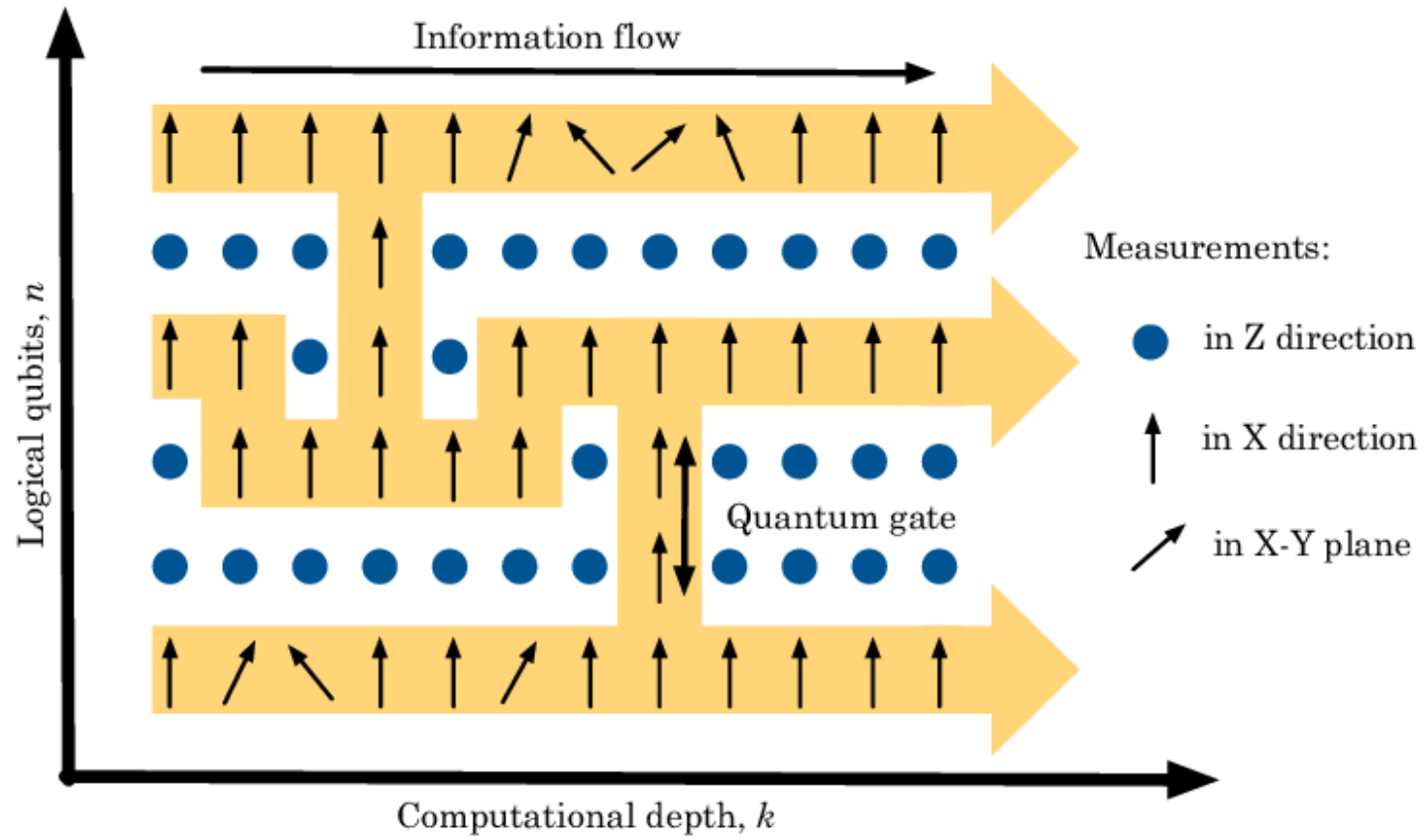
$$|+\rangle = \frac{1}{\sqrt{2}}(|0\rangle + |1\rangle)$$



Applying  
CZ gate

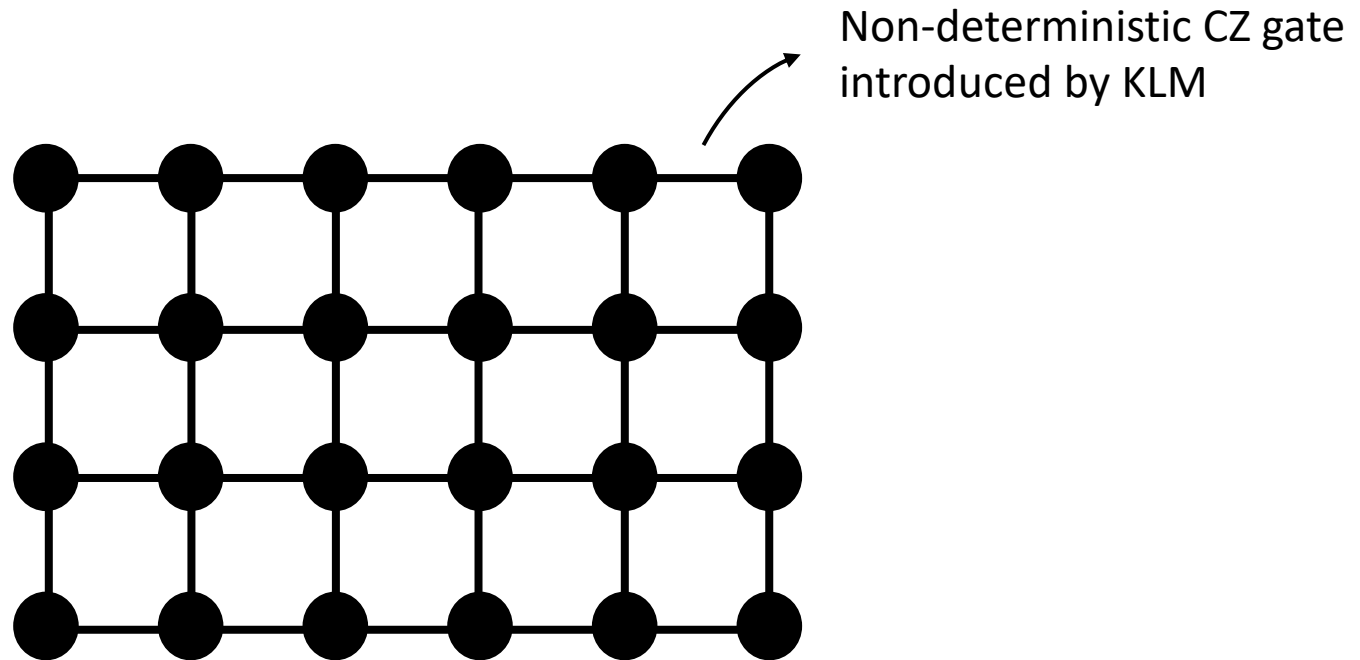


# Computation on Cluster states



# *Nielsen Protocol*

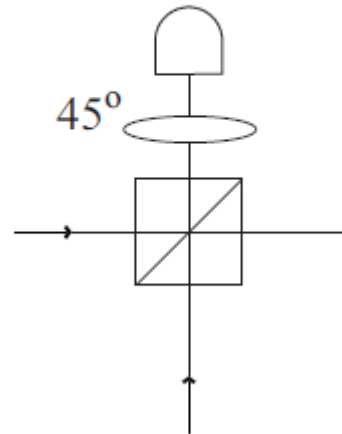
Nielsen introduced a protocol for generating cluster states using the probabilistic KLM CZ gate.



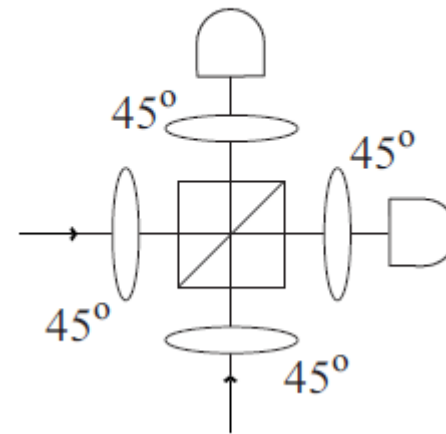
# *Brown-Rudolph Protocol*

Browne and Rudolph introduced a protocol for generating cluster states using the probabilistic Fusion gate I & II.

a) Type-I

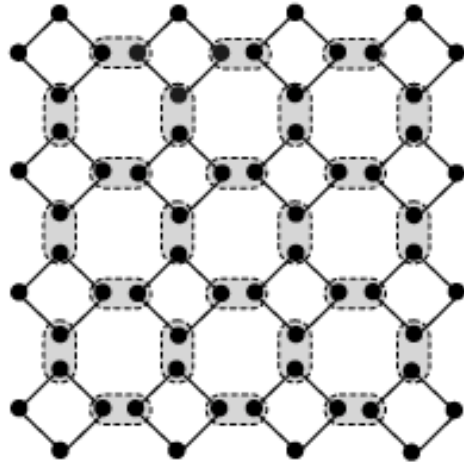


b) Type-II



# Fault tolerant fusion based quantum computation

A model of universal quantum computation in which entangling measurements, called fusions, are performed on the qubits of small constant-sized entangled resource states.



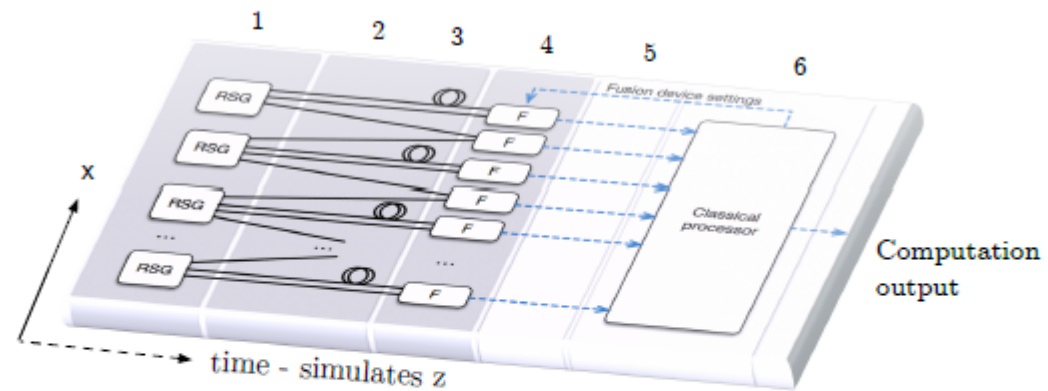
i) Resource states



ii) Fusion measurements



b) Fusion based quantum computing architecture



1. Resource state generators repeatedly create entangled states



2. Fusion network router sends qubits to fusion locations.



3. Delays - delay a qubit for one or more clockcycles.



4. Fusion devices perform reconfigurable measurements



5. Classical signal transmission



6. Classical Processor - decoding and algorithm feedforward

# *Photonic approach and FBQC advantages*

- Qubits should survive a constant depth of manipulation.
- Success of computation is independent of algorithm's depth.
- The encoding can handle all physical error and nondeterministic nature of measurements in a fault tolerant way with a relatively high threshold for errors.
- Photons are best candidate for quantum communication so chip connectivity is much easier, which is very good for scalability.
- No need for aggressive cooling to mK temperature.
- Manufacturing photonic chips is very compatible with already existing fabrication industry.



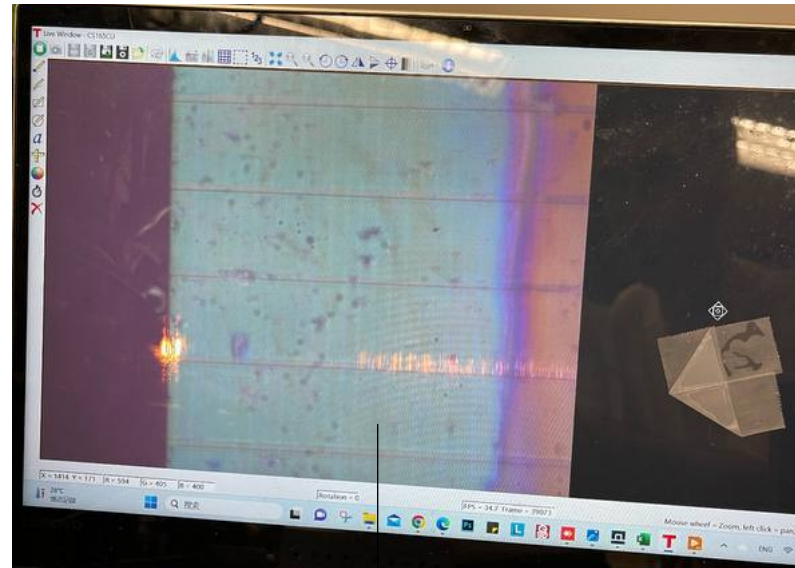
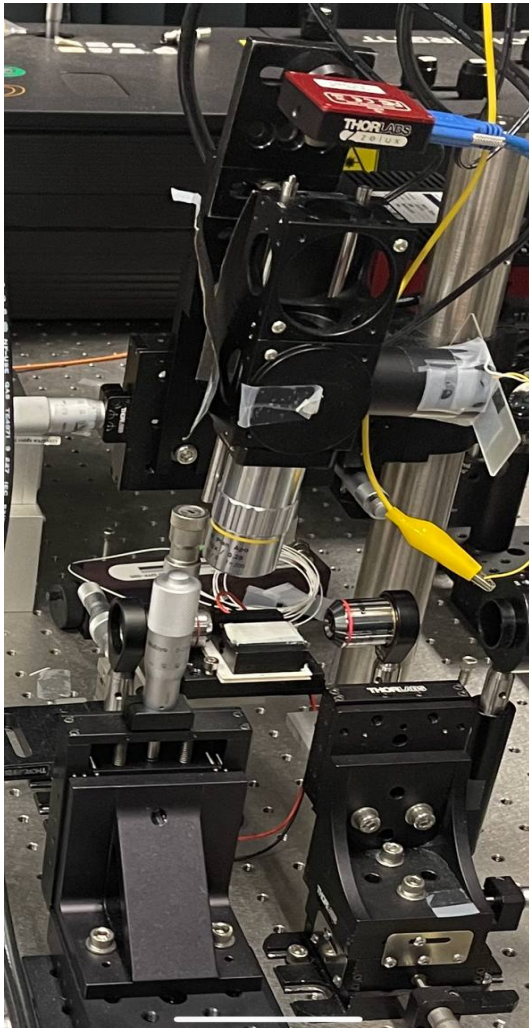
## *Disadvantages of other matter based approach*

- Decoherence time!
- Lack of proper connection between far qubit on hardware, which results in implementing large number of unnecessary swap gate in algorithms.
- In matter based approaches qubits can go out of the encoded state which results in errors that can not be corrected.
- Communications between chips is still a problem.

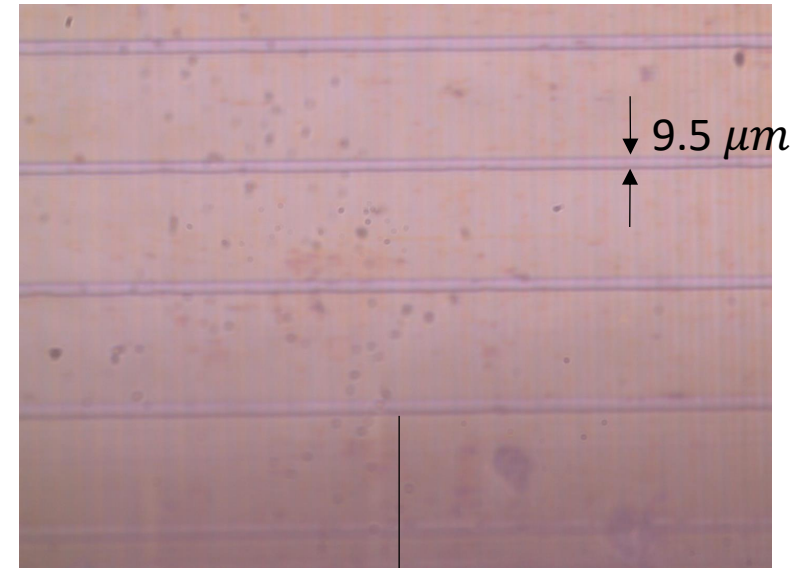
# *Experimental experiences*

*Type 0 SPDC*

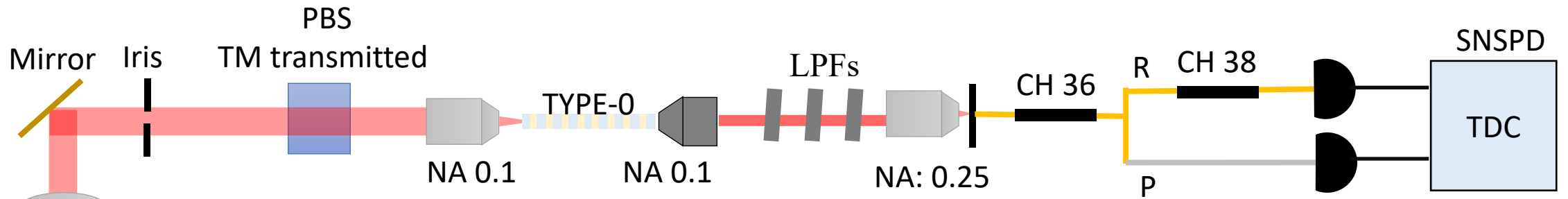
# TYPE-0 ppln waveguide



Coating



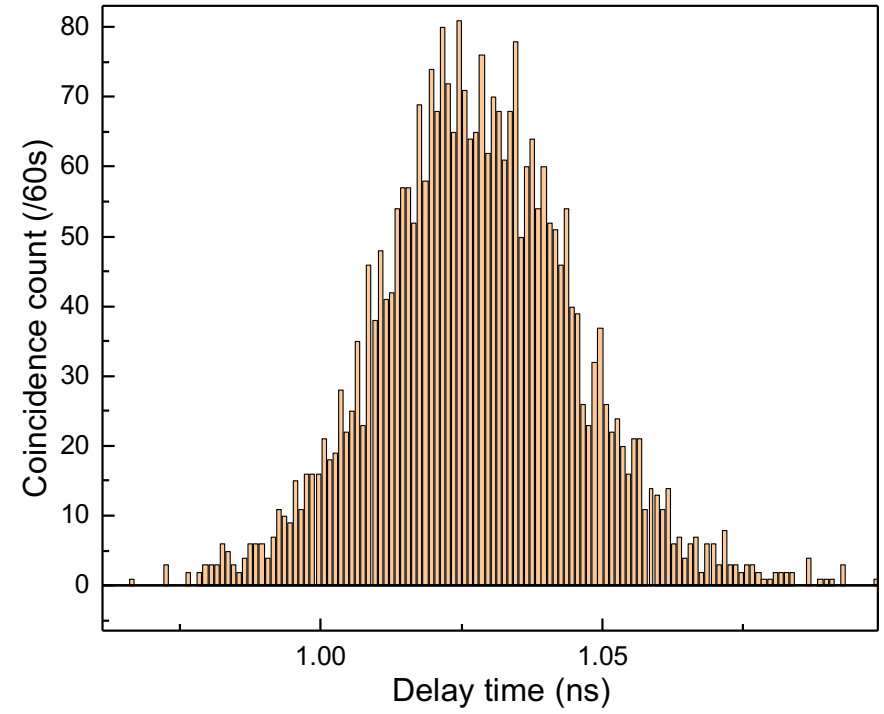
waveguide



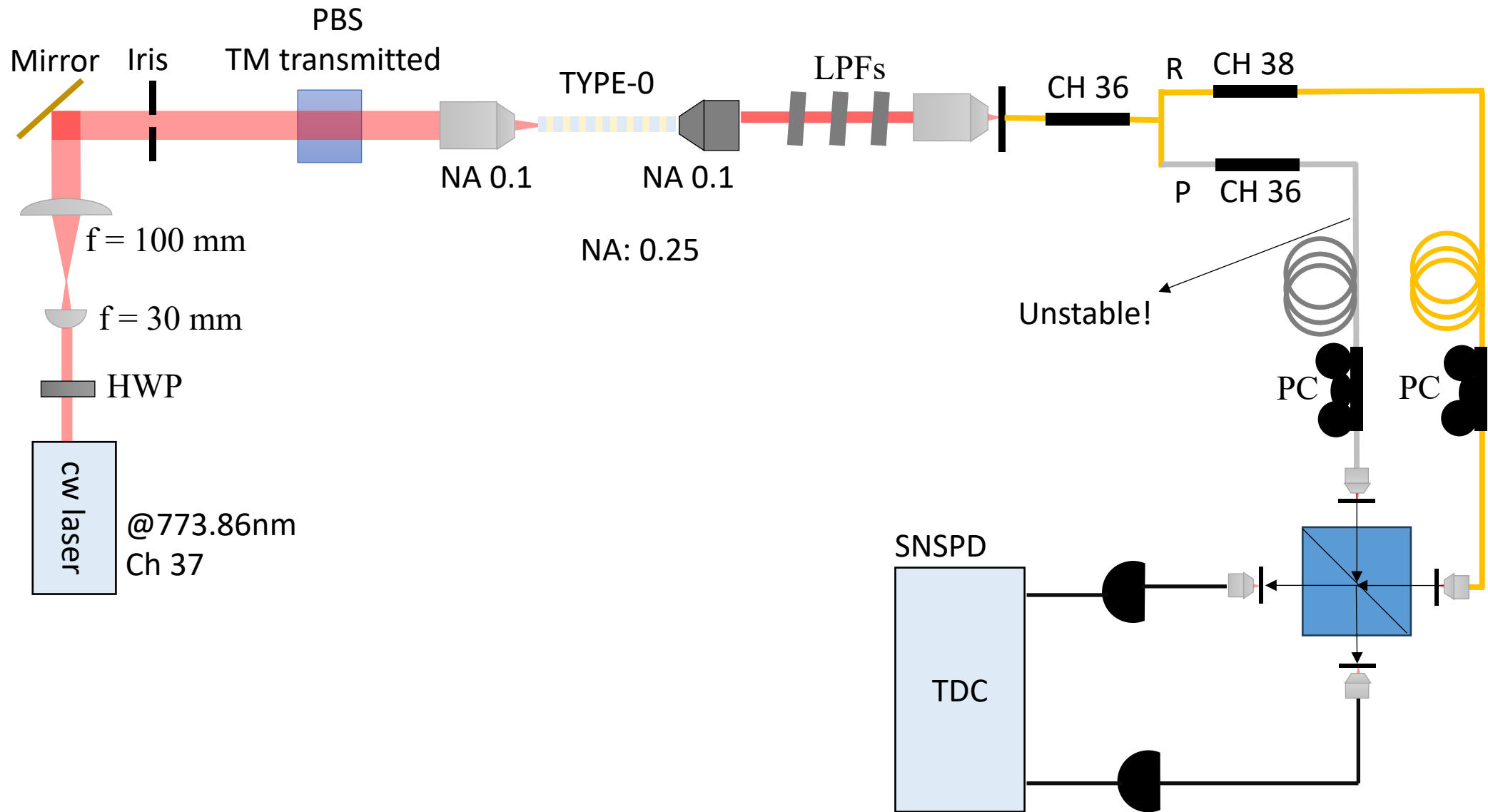
$f = 100 \text{ mm}$   
 $f = 30 \text{ mm}$   
 HWP  
 cw laser @773.86nm  
 Ch 37

Channel	Frequency	Wavelength
#	GHz	nm
36	193600	1548.51
38	193800	1546.92

Input power ~1 dBm

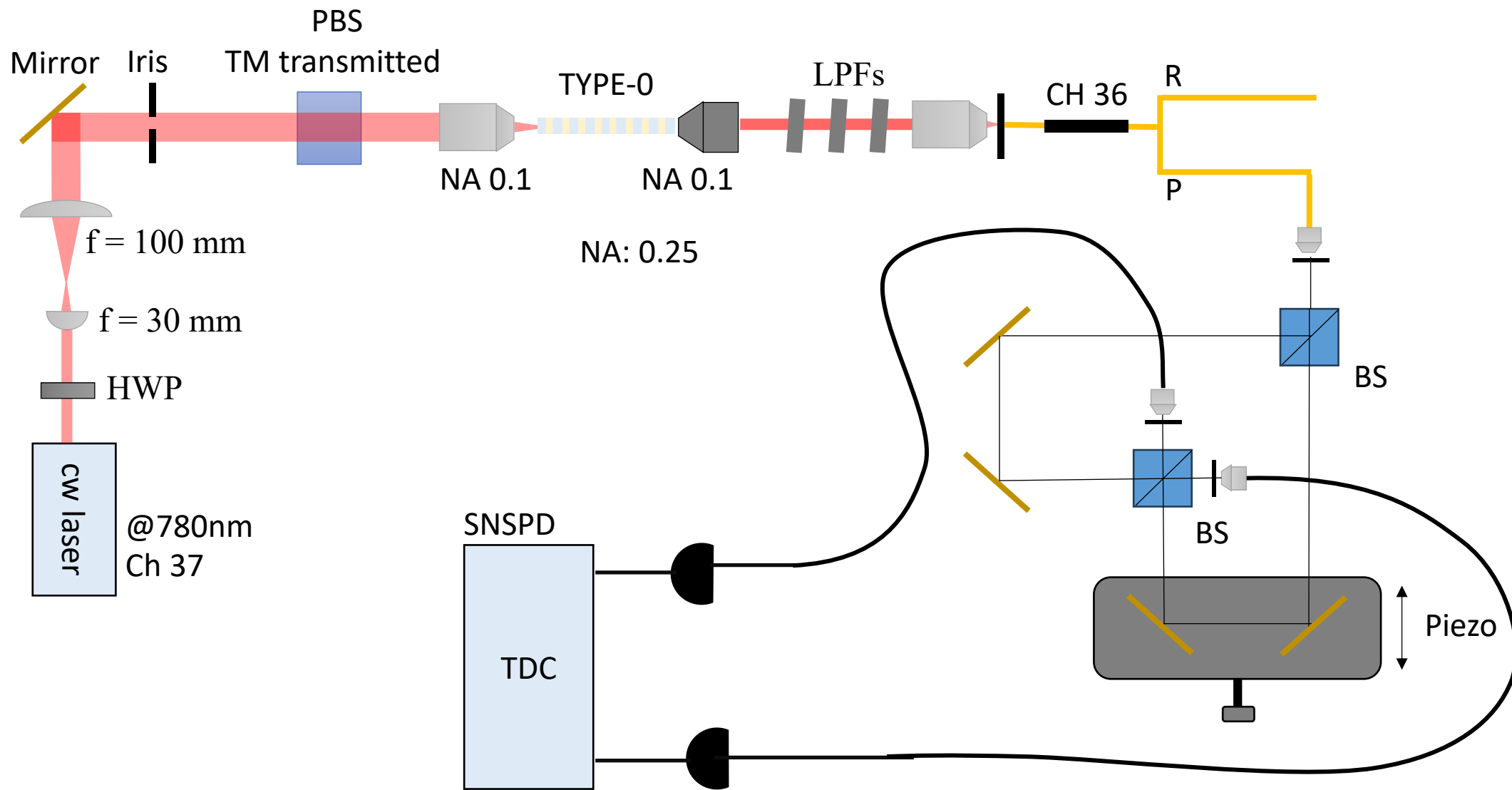


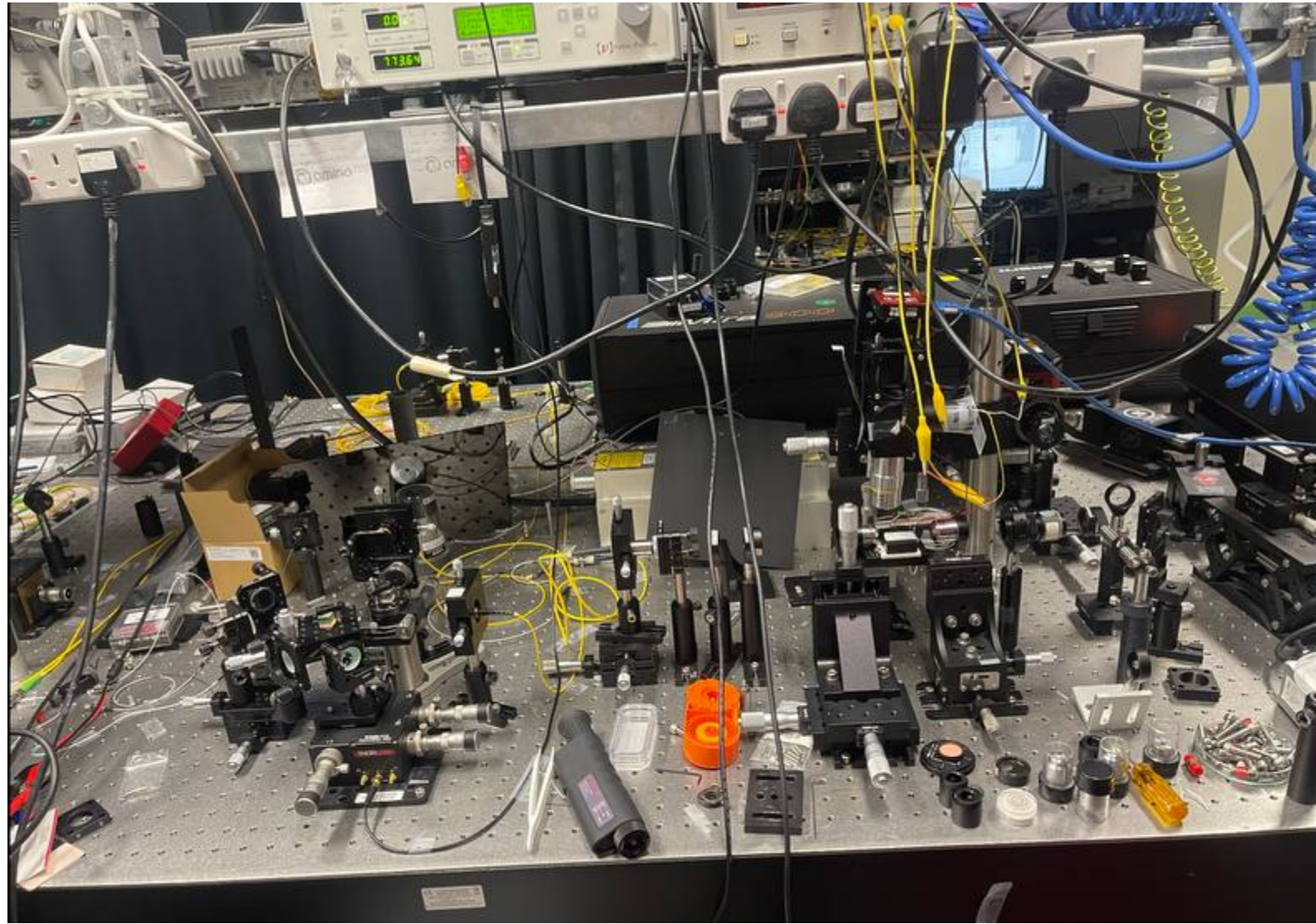
# *HOM interference*

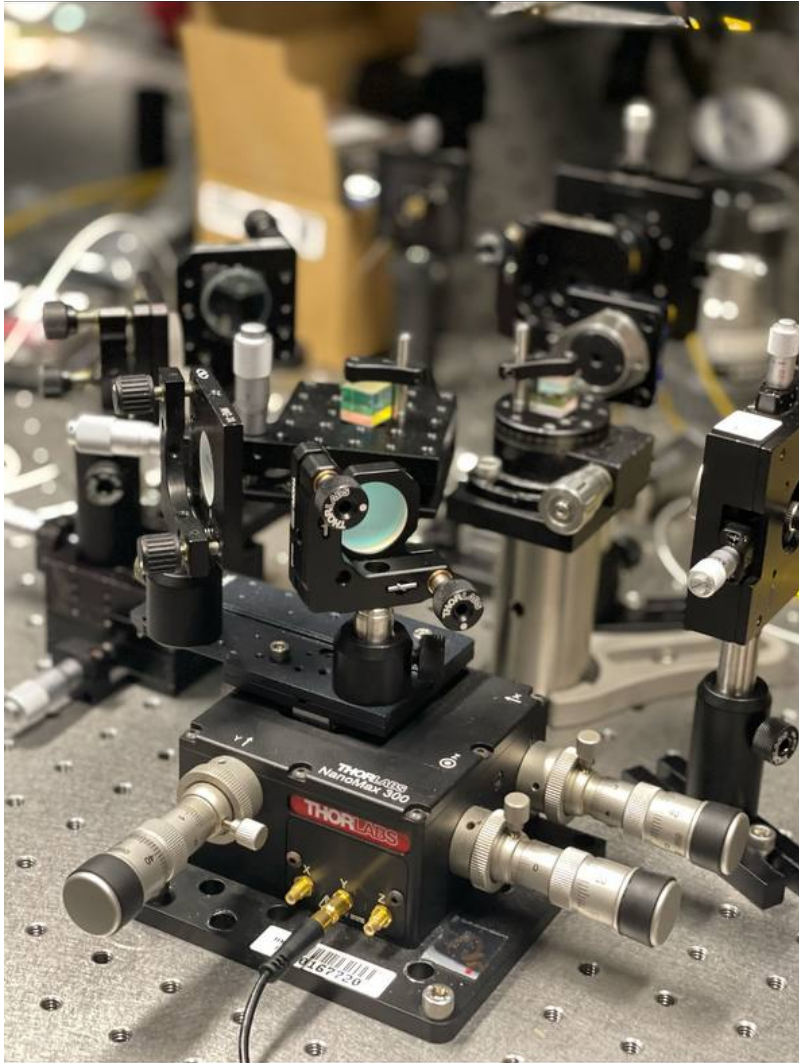


# *Franson interference*









### The states:

$$|\psi\rangle = |L\rangle_s |S\rangle_i$$

$$|\psi\rangle = |S\rangle_s |S\rangle_i$$

$$|\psi\rangle = e^{i(\varphi_s + \varphi_i)} |L\rangle_s |L\rangle_i$$

$$|\psi\rangle = |S\rangle_s |L\rangle_i$$

### Delay:

$-\Delta\tau$

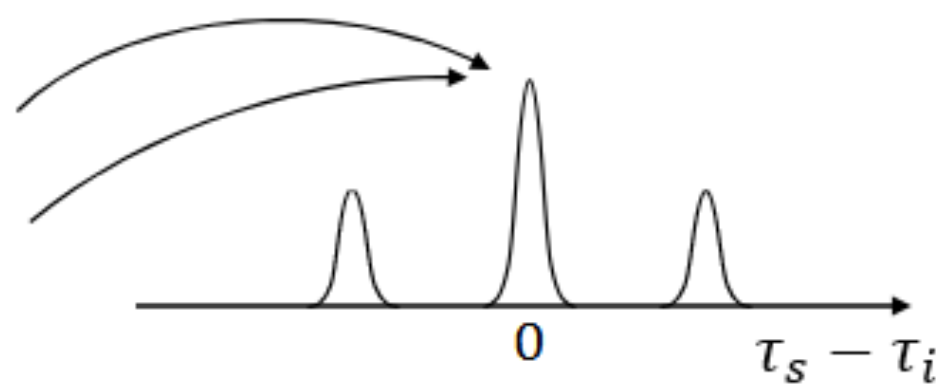
0

0

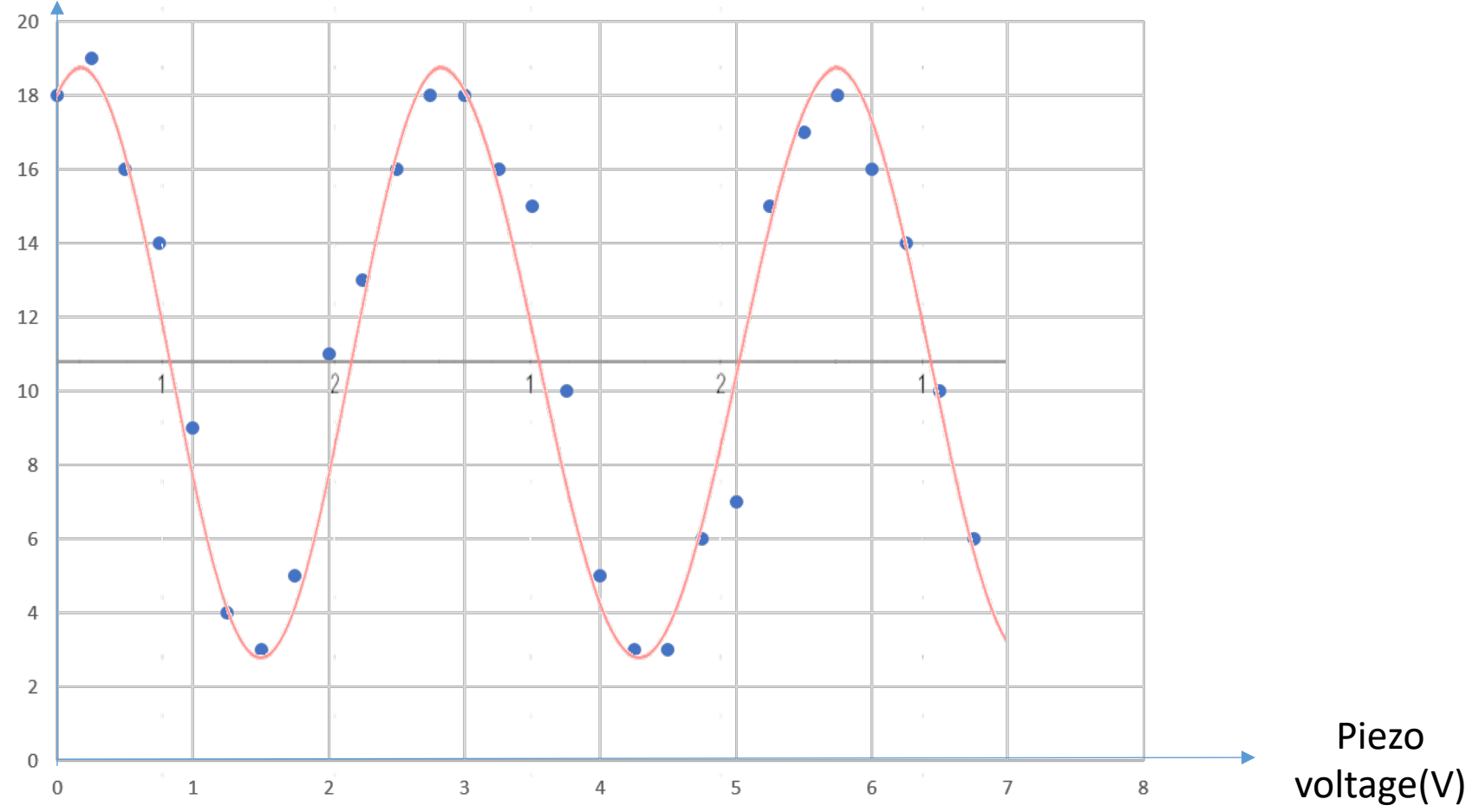
$\Delta\tau$

### Histogram:

PD2

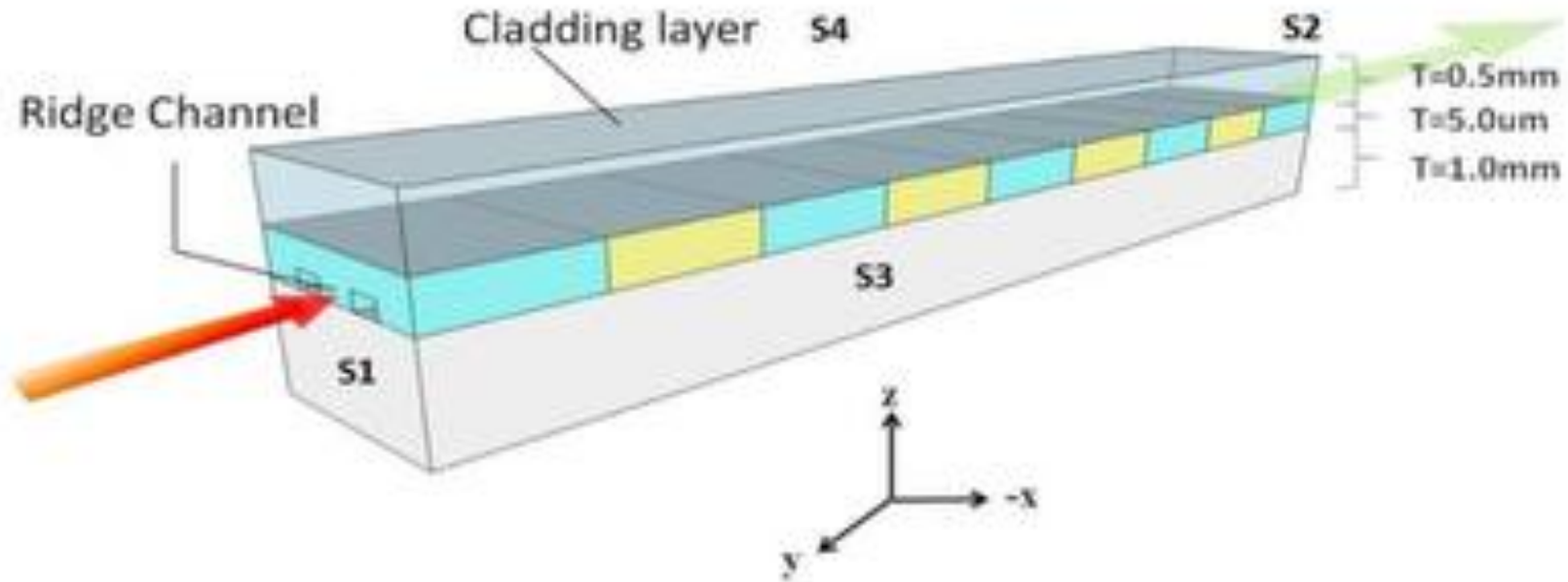


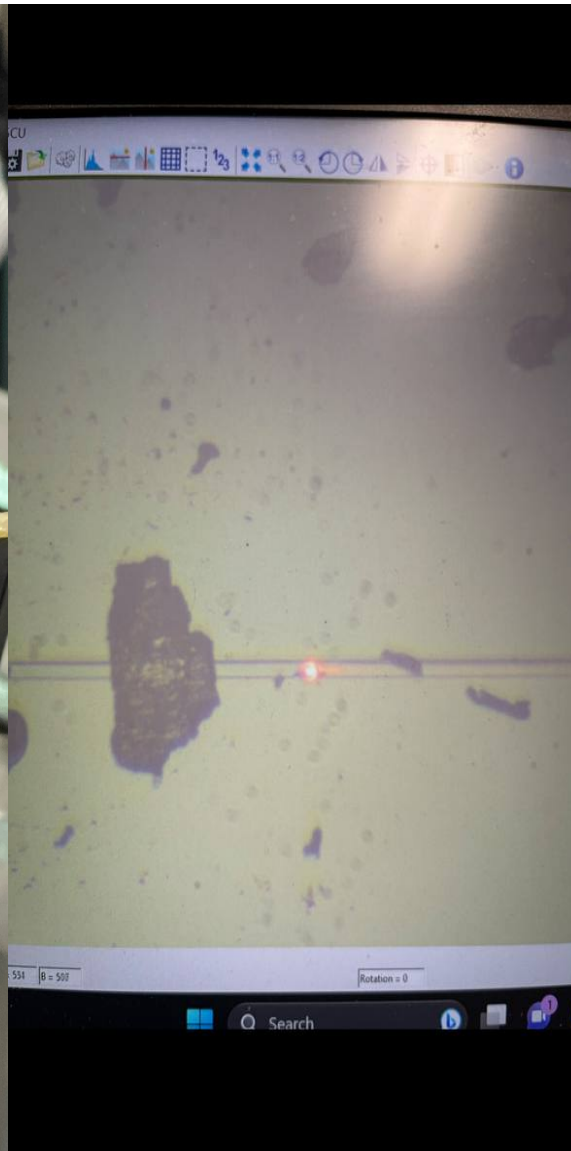
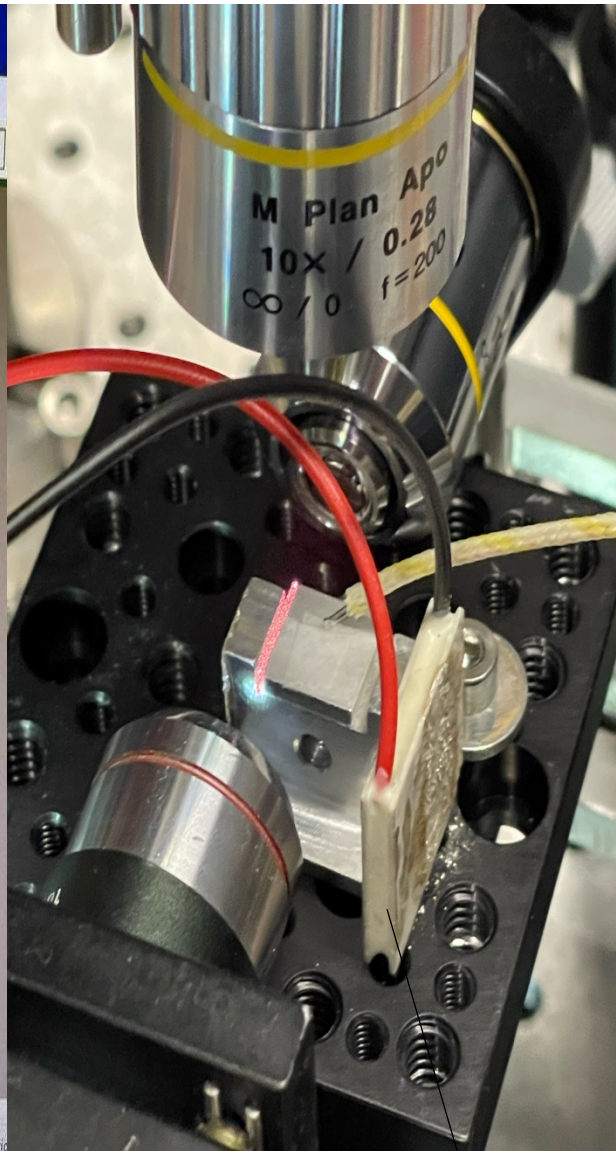
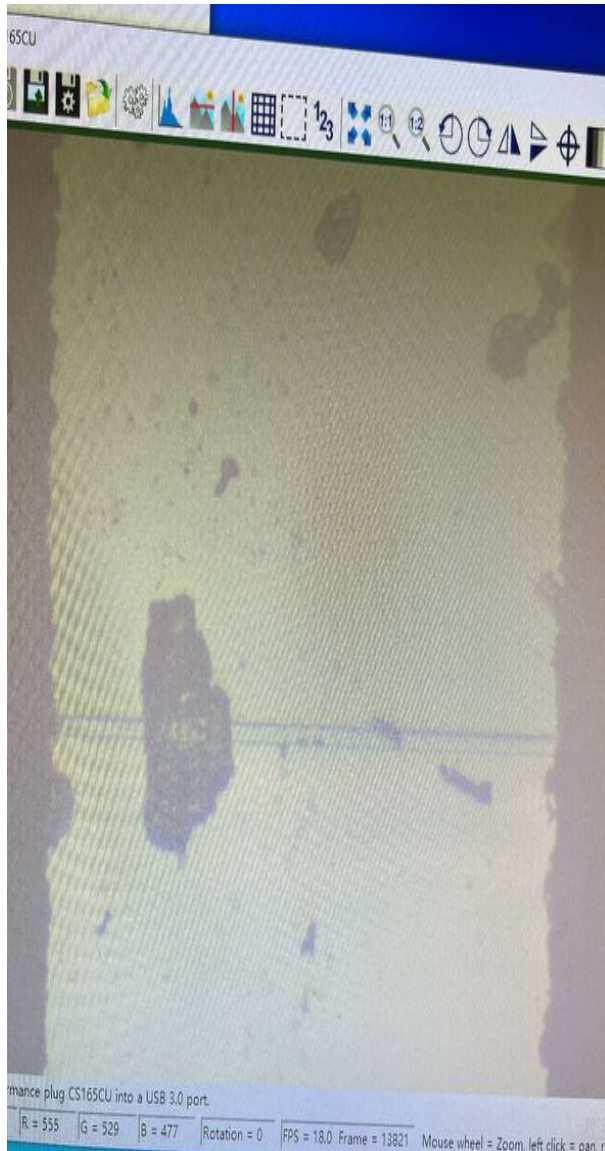
Coincidence  
count



*Type 2 SPDC*

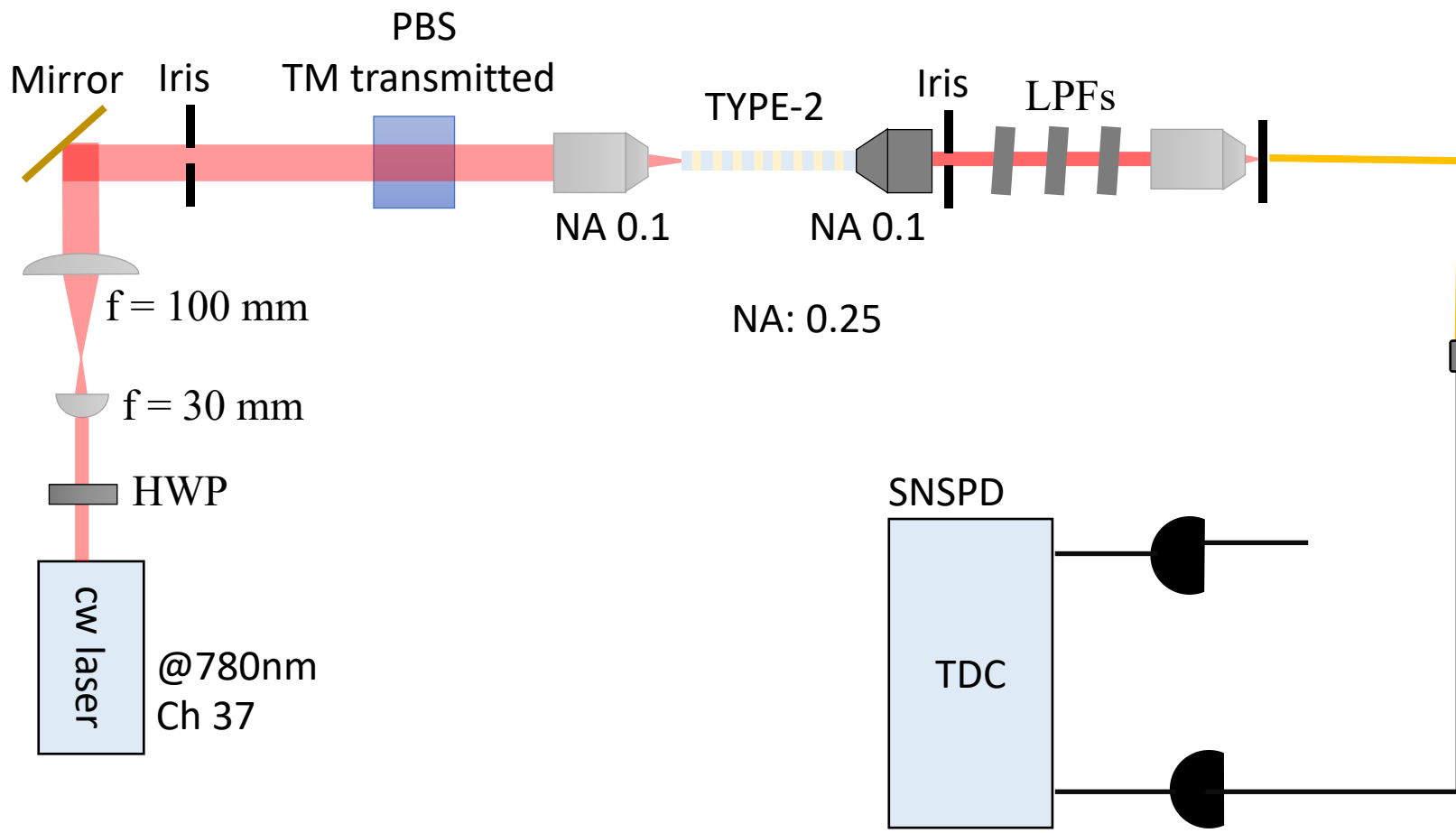
# TYPE-2 ppln waveguide



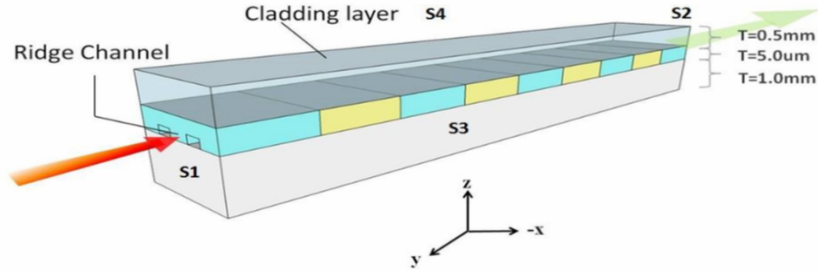


TEC





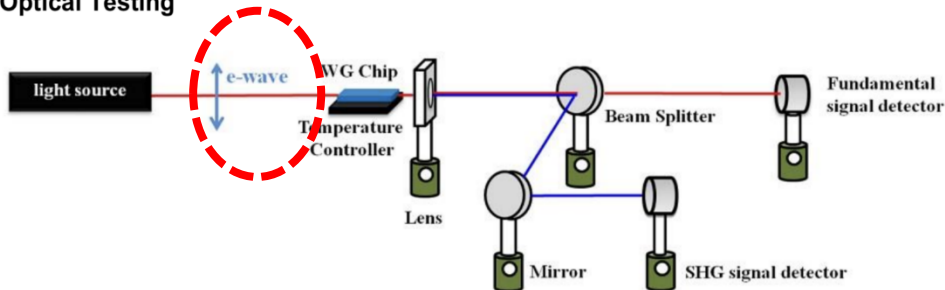
**B. Chip Physical Structure**



**Note :**

1. Please avoid moving the chip with respect to the laser beam during high power operation.
2. Please handle the chip with the S3/S4 facets and avoid contacting both input/output (S1/S2) surfaces.

**D. Optical Testing**

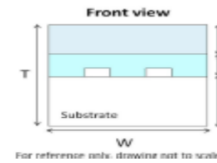
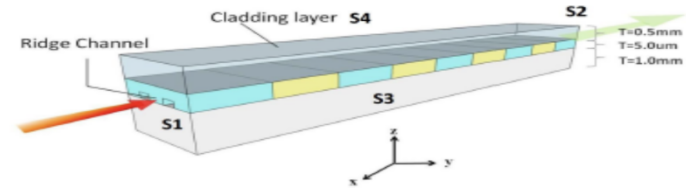


following items—

1. Please refer to the attached test report and confirm if the light was coupled at the right position.

2. For type-2, the optic axis goes like 1560nm(o wave)+1560nm(e wave) à 780nm(o wave)

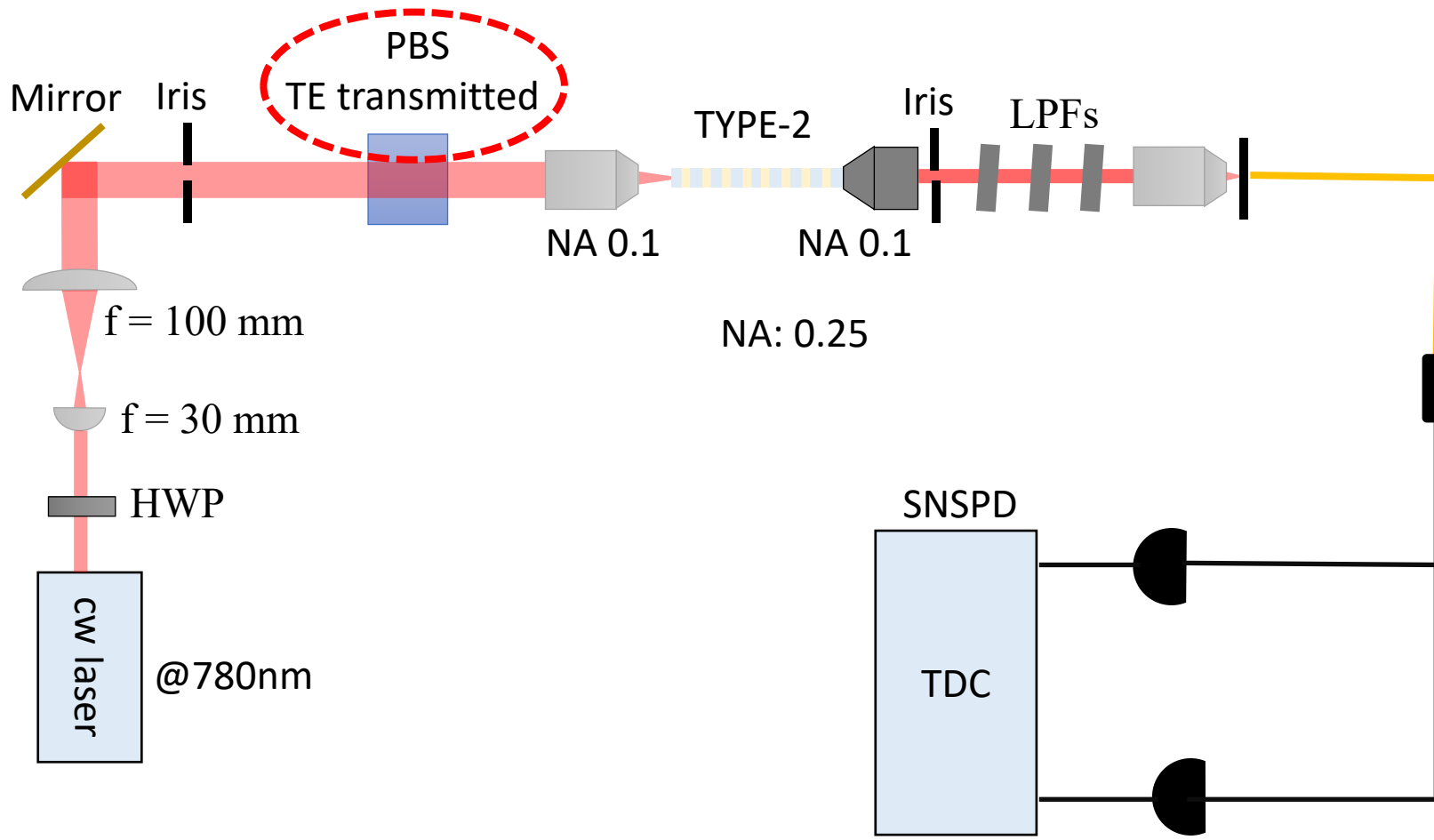
Did your optic axis correspond to it?



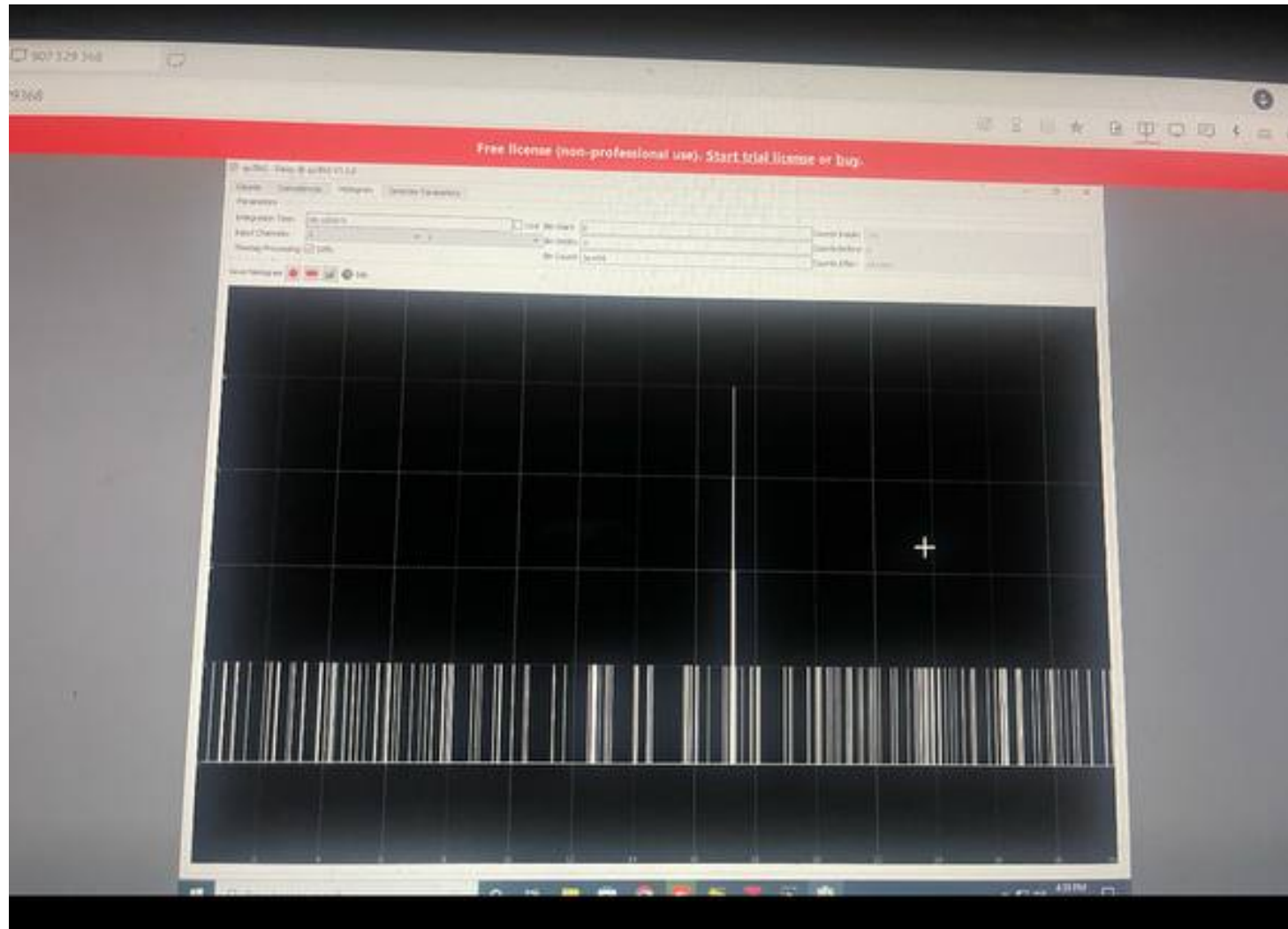
3. The PMT(phase matching temperature), by the test report, is ~31degC. Have you scanned the temperature for optimal output?

	Chip ID	Length (mm)	Wavelength (nm)	Intrinsic efficiency (%/W)	Phase match temperature (°C)	Result
Specification		18	SPDC 780 → 1560+ 1560	-1.8	45~120	
Measured data	AP-Y190102-11-G07-01-06	10.3	SPDC 780 → 1560+ 1560	-2.2	~31	PASS

Reply to All

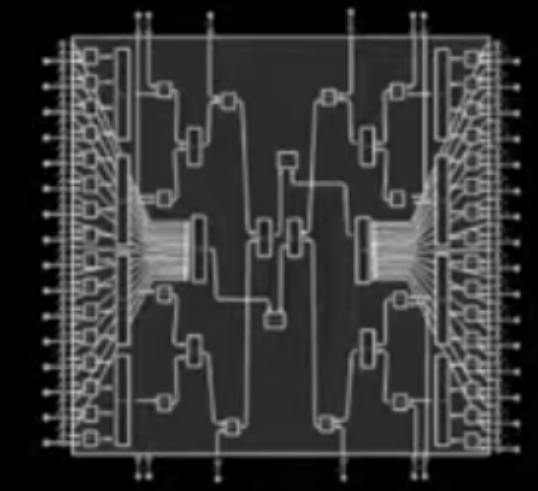
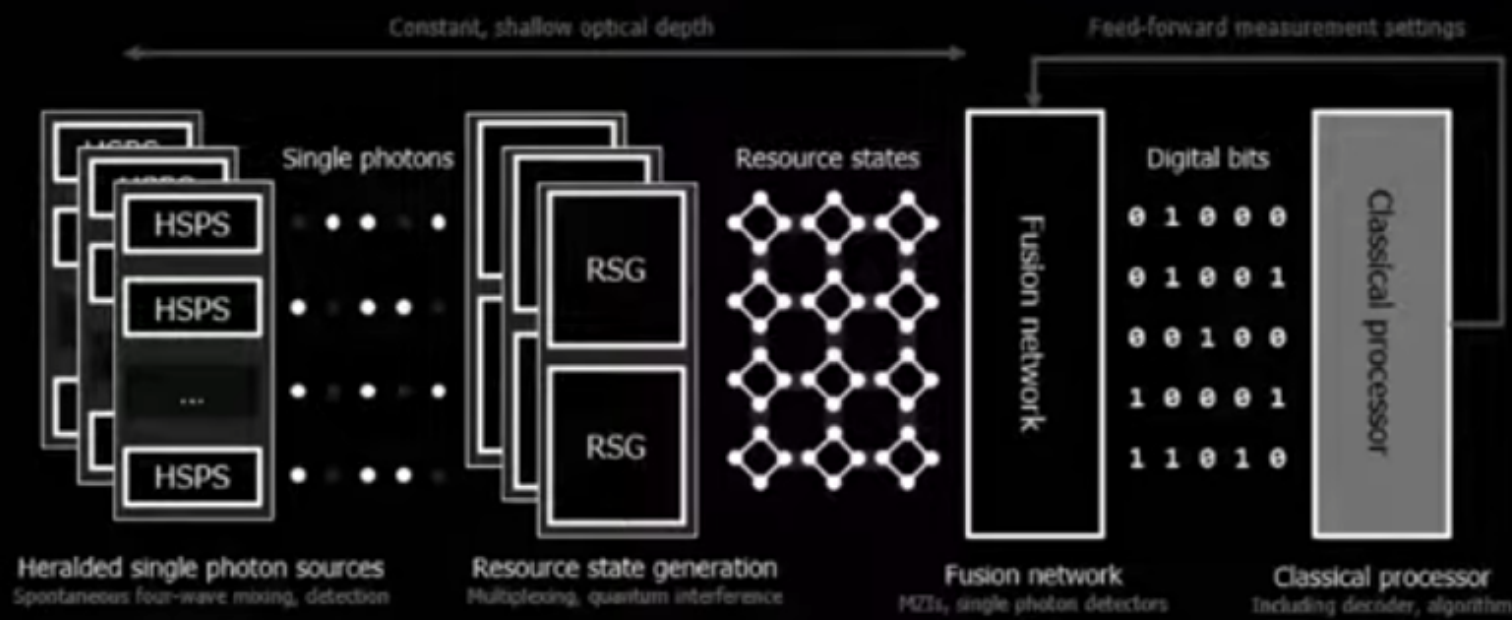


## Coincidence for type 2

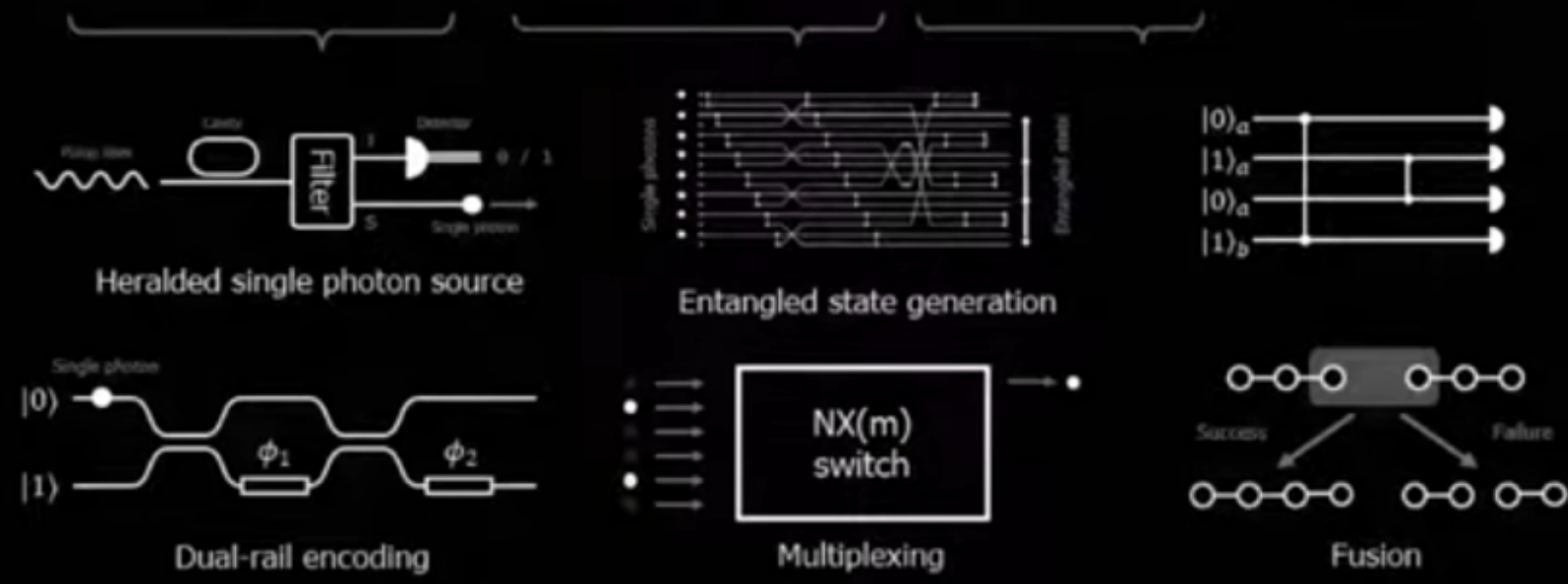


*State of the art and challenges*

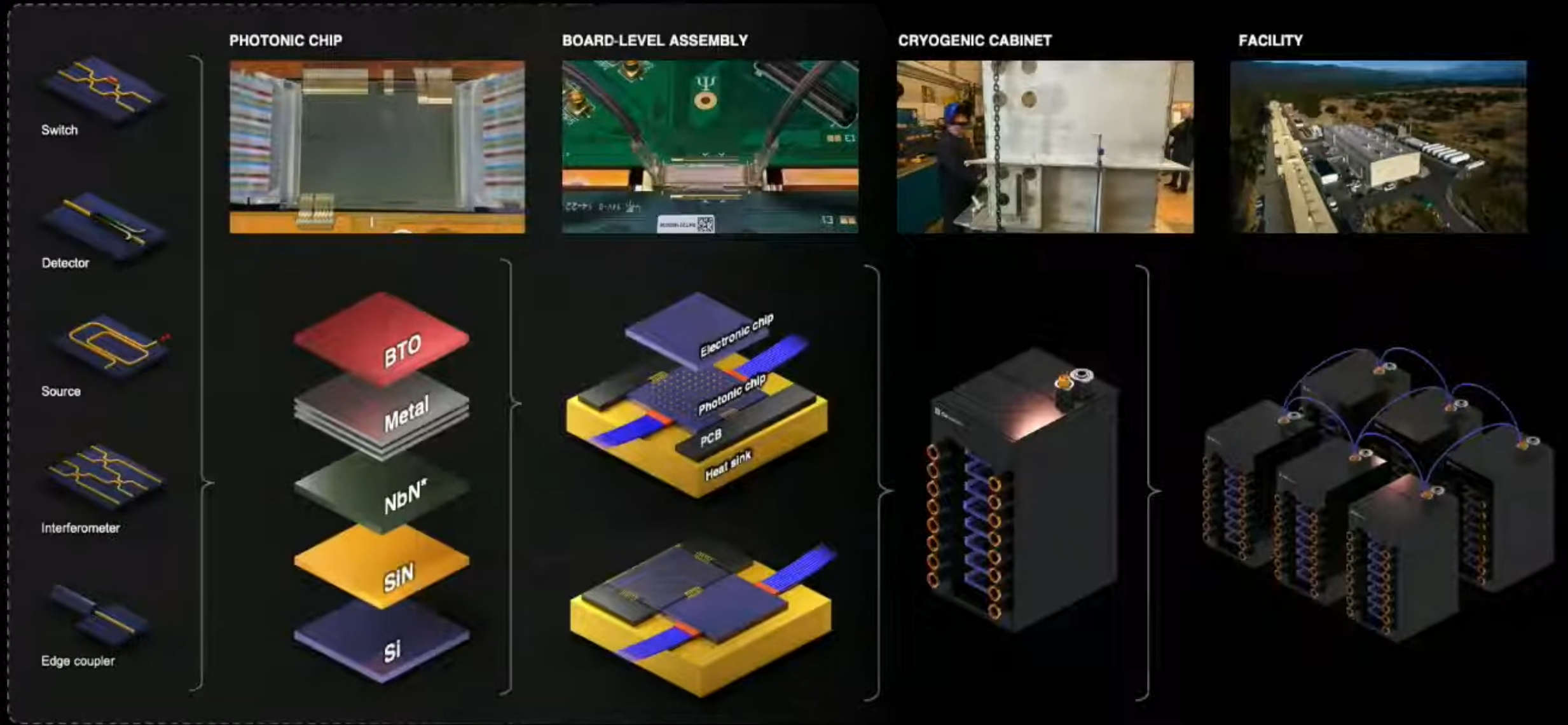
 **Psi**Quantum



Example of physical layout of unit cell



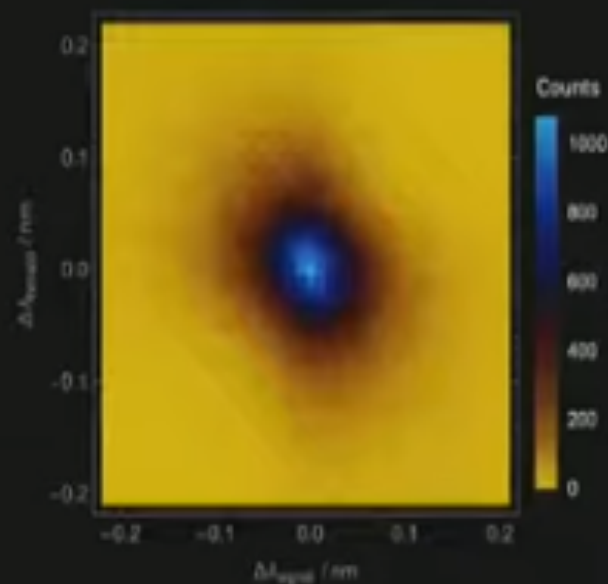
# Map of a photonic quantum computer





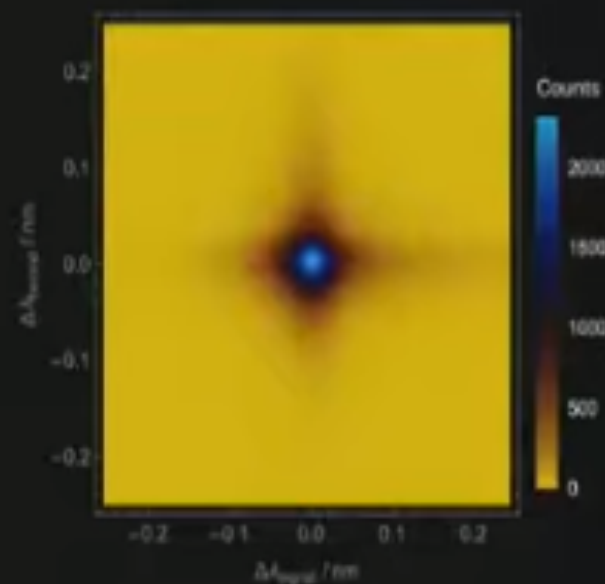
# Single photon generation

SCOPE OF EXISTING HARDWARE →



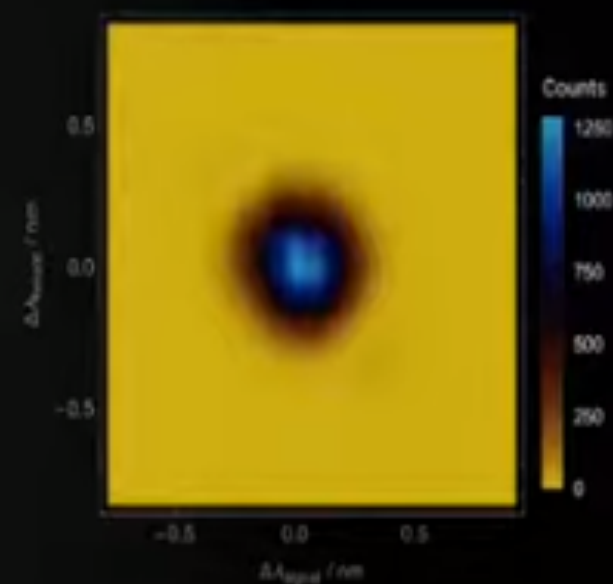
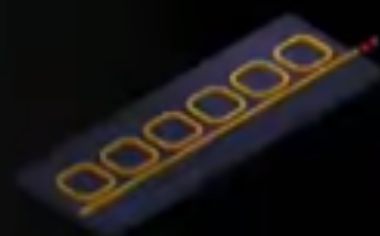
~90% purity

EXPERIMENT



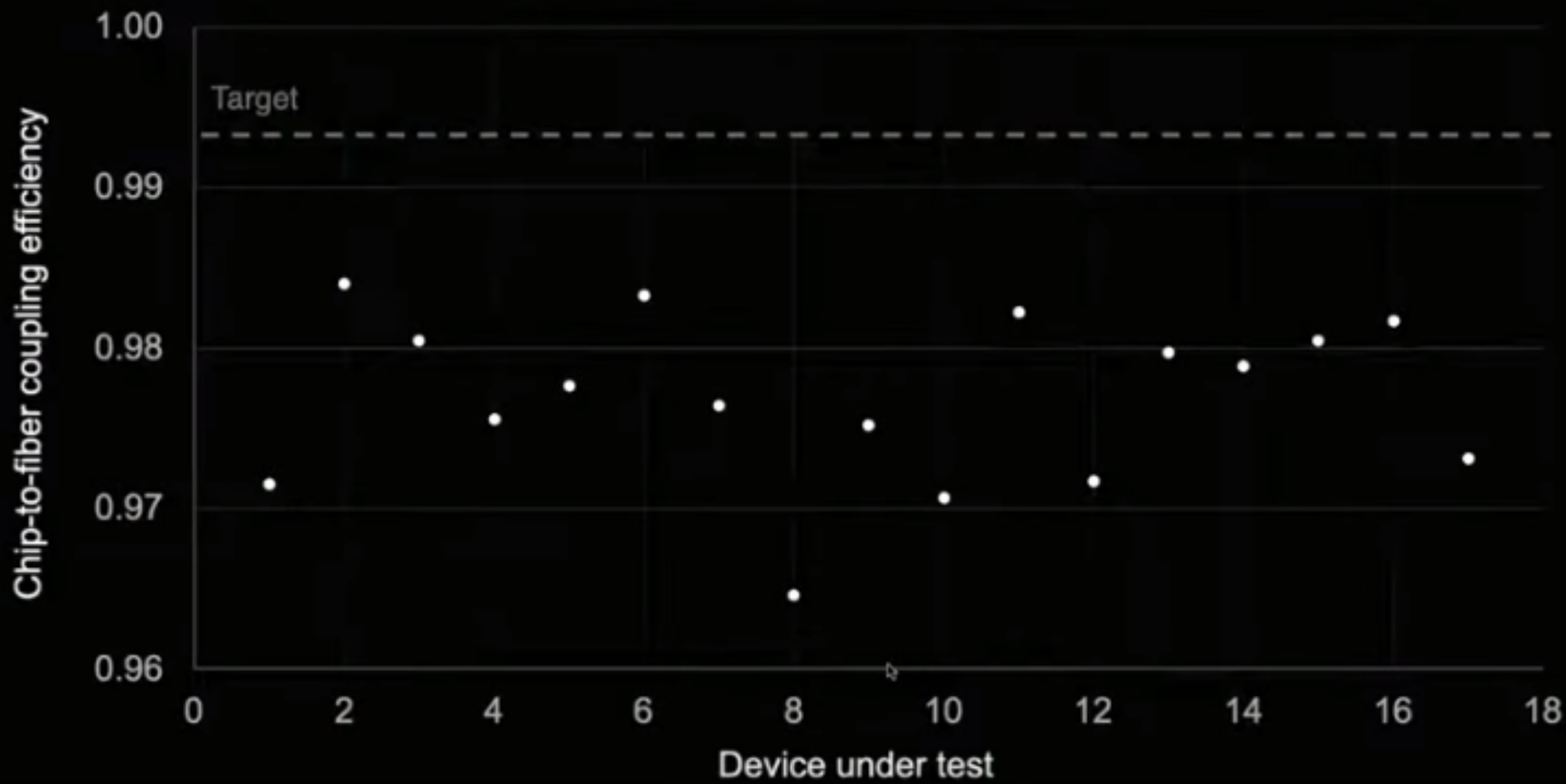
~99.5% purity

EXPERIMENT

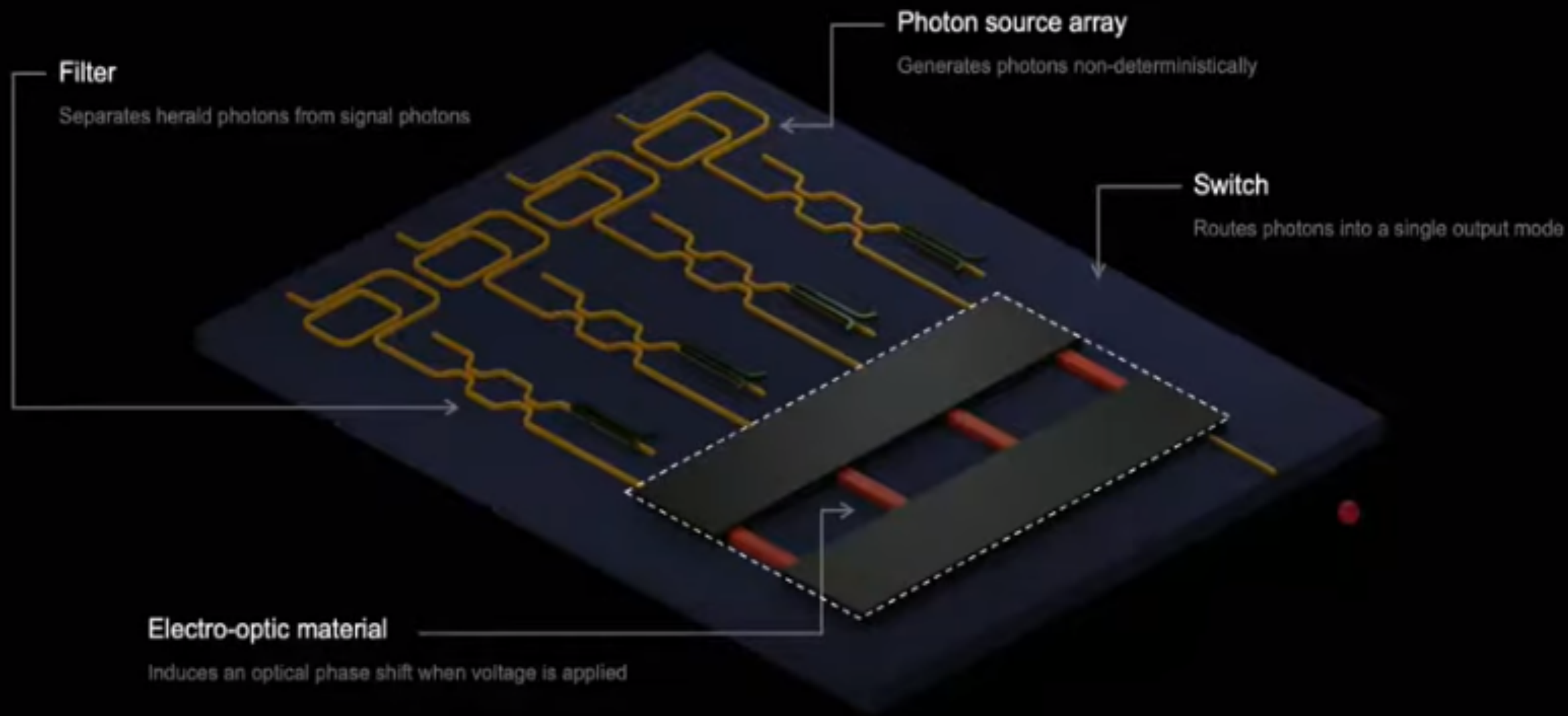


~99.9% purity

SIMULATION



# Optical switching

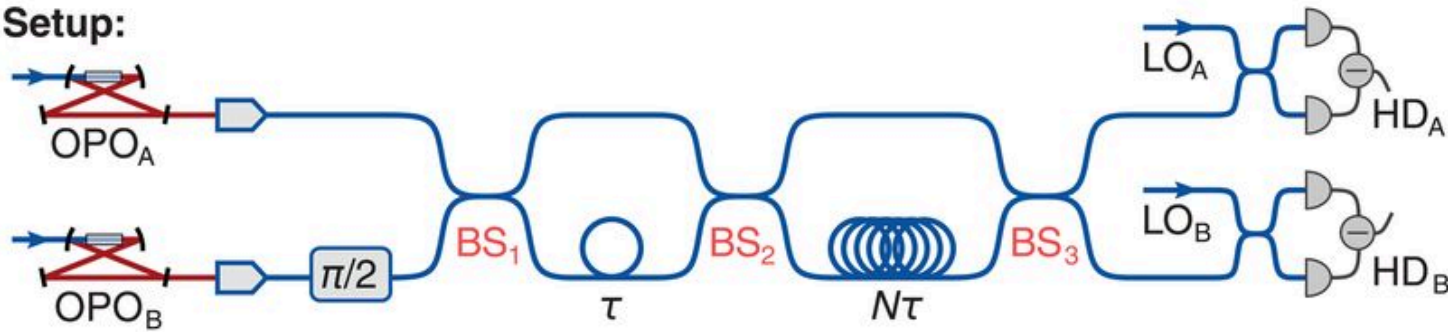




XANADU

# Deterministic generation of cluster state

Setup:



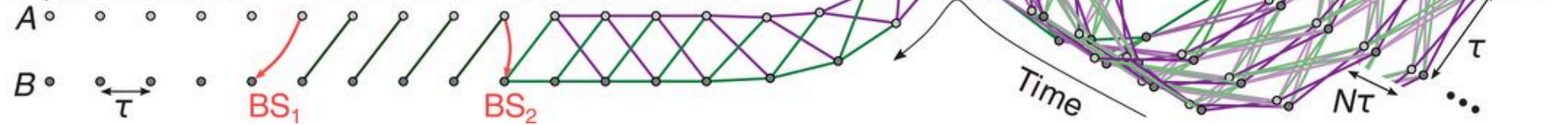
Resulting graph:

- 1 —
- 1/2 — 1/2 —
- 1/4 — 1/4 —

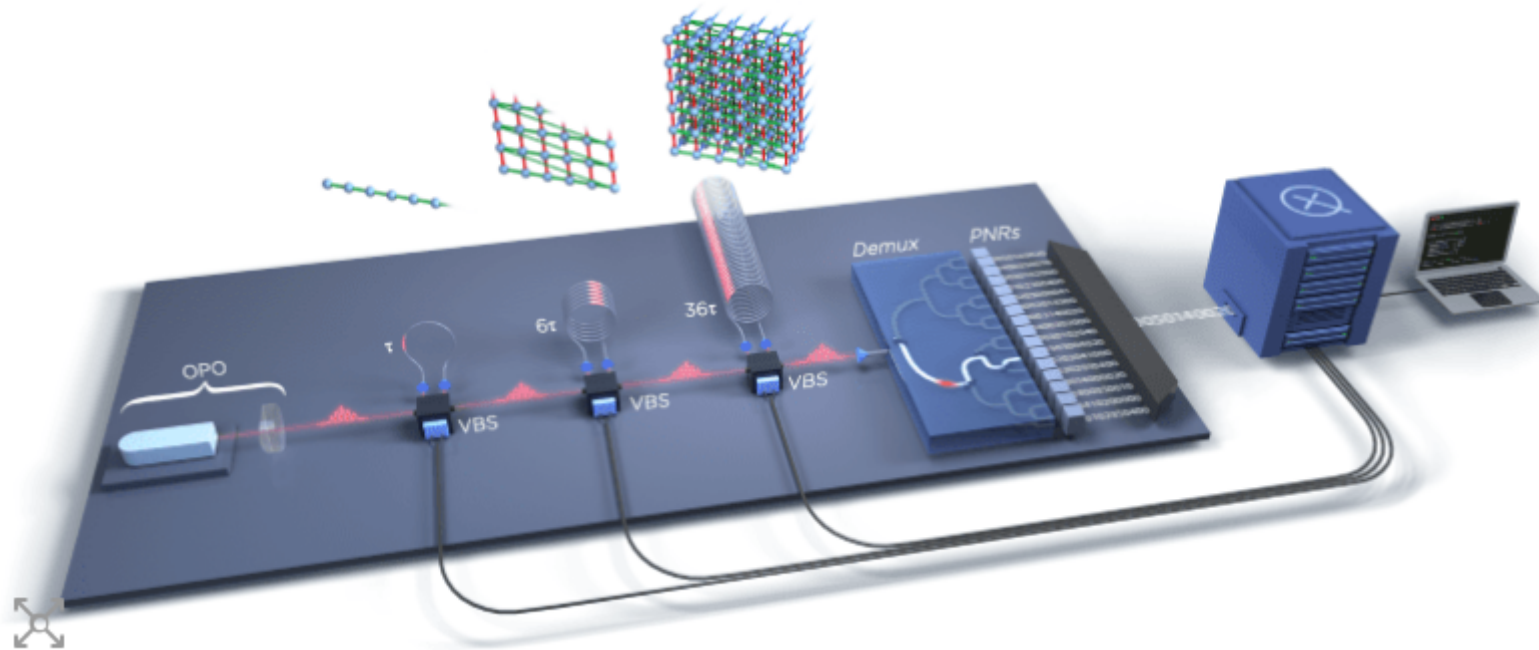
Squeezed states

EPR states

1D cluster state



# Borealis



*Thanks for your attention!*